

Abstracts

A low voltage RF receiver for digital satellite radio (1998 [RFIC])

G. Calì, G. Cantone, P. Filoramo, G. Sima, P. Vita and G. Palmisano. "A low voltage RF receiver for digital satellite radio (1998 [RFIC])." *1998 Radio Frequency Integrated Circuits (RFIC) Symposium 98. (1998 [RFIC]): 301-304.*

An integrated low voltage RF receiver for the Digital Satellite Radio is presented. It uses innovative solutions for critical blocks such as LNA, IF buffer, VCO, etc., and includes power supply regulators at 2.4 V. The circuit has been integrated in a high performance 20-GHz silicon bipolar technology and correctly operates with an external power supply varying from 2.7 V to 5.5 V.

 [Return to main document.](#)